

PART-TIME JOB OPPORTUNITY for Electronics/Engineering/Science/English major

Hours: 10 to 30 hours per week

Pay: \$16/hour plus reimbursement for mileage & travel

Location: Santa Rosa (by the Santa Rosa Airport)

Duration: 1 year minimum, up to 2 years. Starting July or August '07.

Job Description:

Intern to assist local test equipment design and assembly company and major Defense/Aerospace company to implement supply chain process improvements. Intern will work at SenarioTek, learn and participate in design, analysis, manufacturing and Supply Chain processes & technology, help to develop process improvements, and document written procedures.

Intern will have opportunity to participate in extensive formal and on-the-job training.

Primary Duties and Responsibilities

- Directly involved in the documentation of software development to meet requirements and product definition.
- Work with cross functional team members to understand requirements and write product definition and specification documents. Communicate and verify these specifications with the electrical engineers and test technicians.
- Document automated test scripts that exercise code to developed and integrated hardware.
- Document user interface designs and code per qualified instructions. Instructions can be written or verbal.
- Assist in the tracking of software improvements and version control.
- Create documented instructions for test and operation based on set requirements. Take redline mark ups and create updated documents to assure test technicians and product manual has current and accurate operation instructions.
- Document test requirements per verbal or written instructions.
- Responsible to use ESD protection devices and follow ESD procedures while handling the product hardware and internal circuitry.
- Provides recommendations, makes decisions, and/or escalates based on the needs of the situation.

Job Requirements:

Intern must be self-starter with strong interpersonal, communication and writing skills. Must be familiar with MS products including Outlook, Microsoft Word, Excel, PowerPoint; familiarity with Visio and Project a plus. Must be flexible and dependable, must be willing to make a 1 year commitment.

Electronics/engineering/science major with interest in area of electronics design and manufacturing preferred. English major with interests in manufacturing technology and technical writing will also be considered.

This is an opportunity to work on development of test automation, software design technology, design analysis, quality system development and documentation of processes.

Intern will provide own transportation to & from SenarioTek in Santa Rosa.

Interested? Please submit or email resume to Jagan Agrawal; Chairman, Department of Engineering: jagan.agrawal@sonoma.edu

